

PRIOR ART

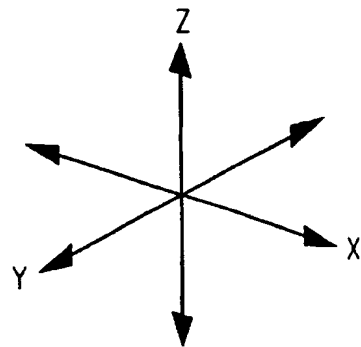
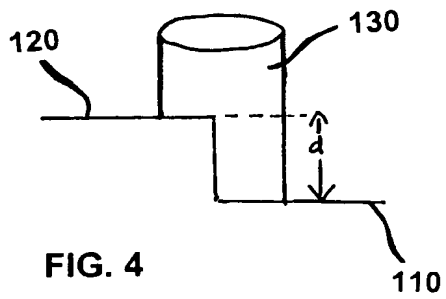
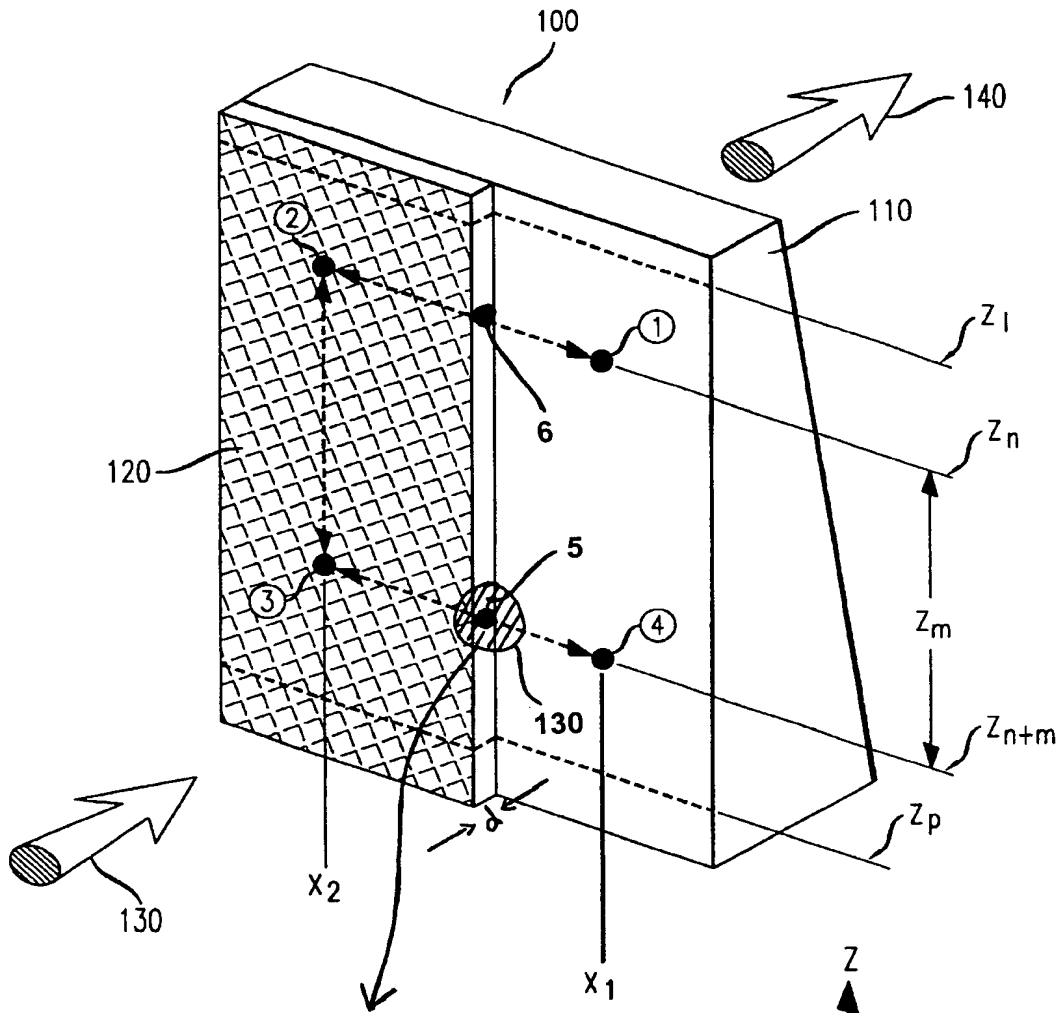
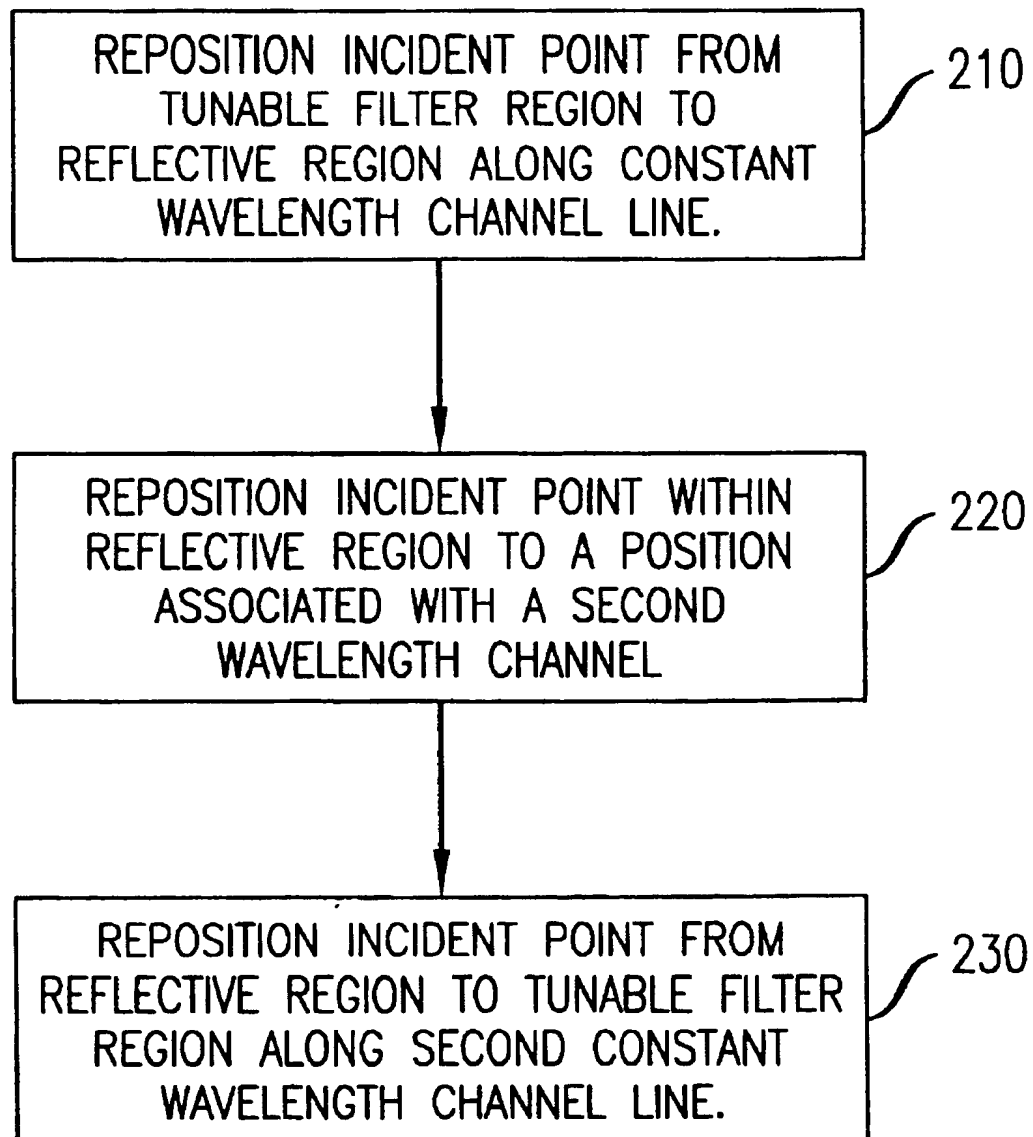
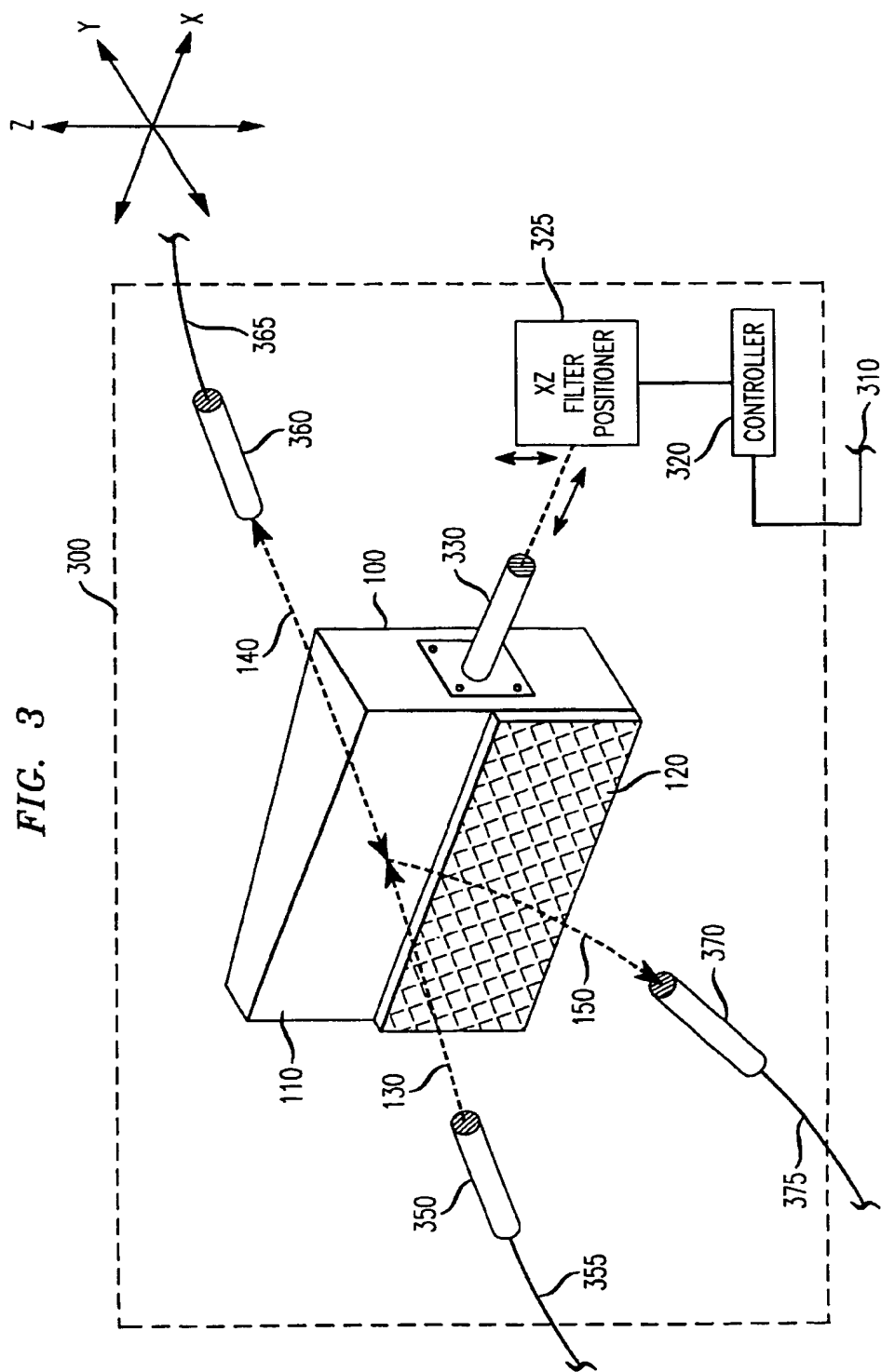


FIG. 2

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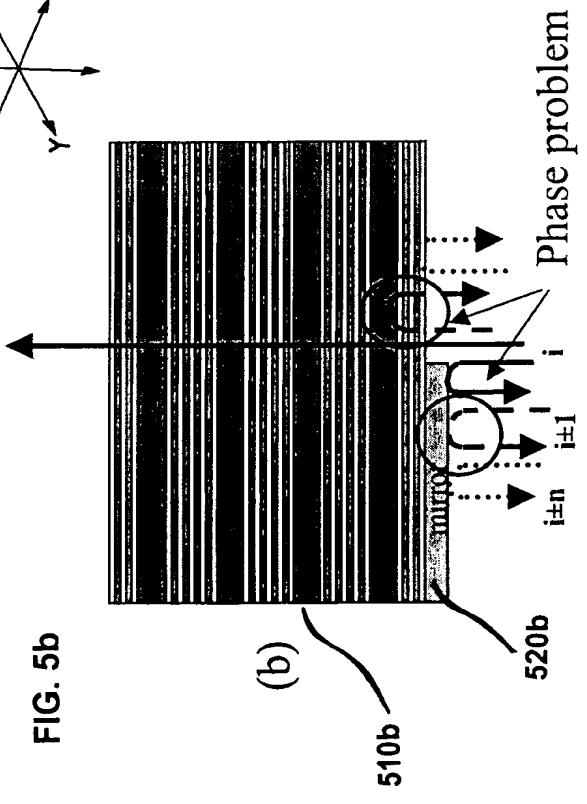
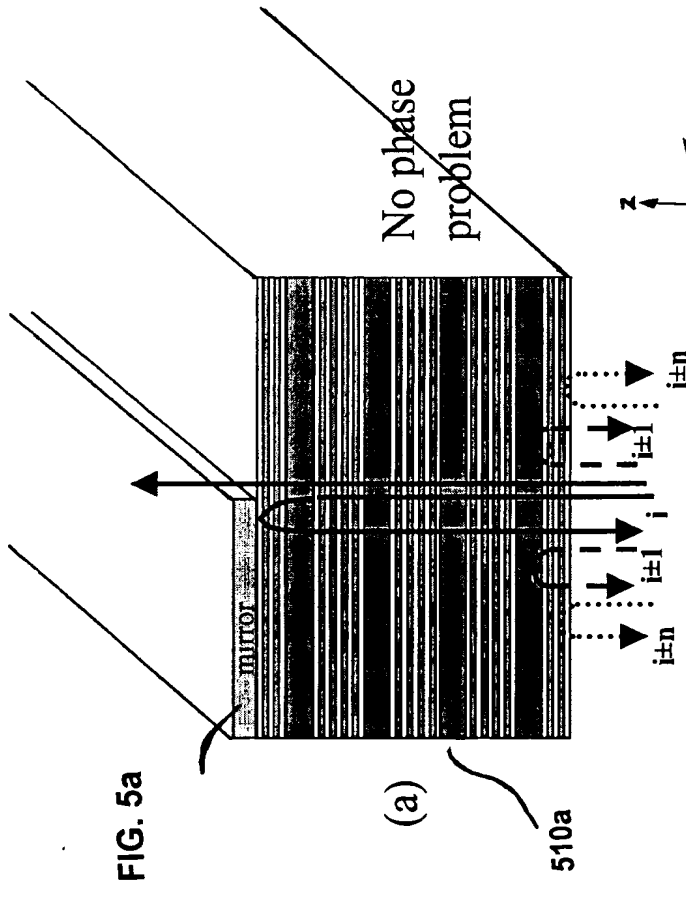


FIG. 5c

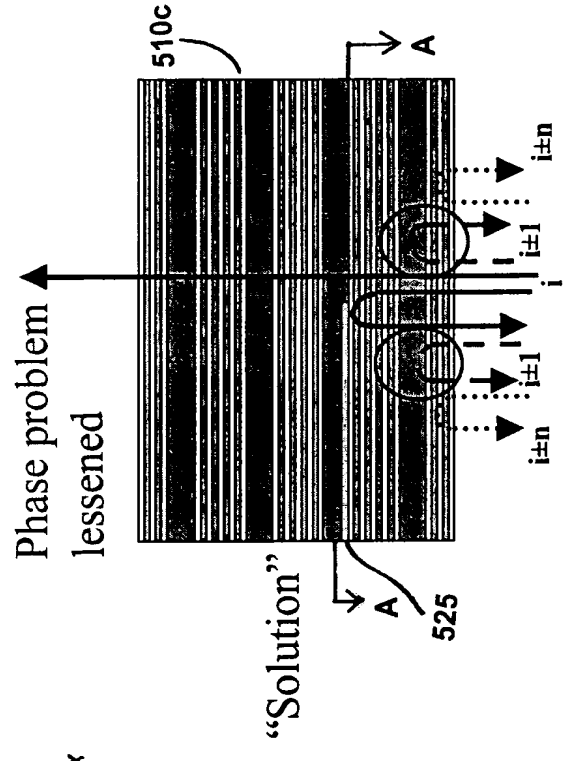


FIG. 6a

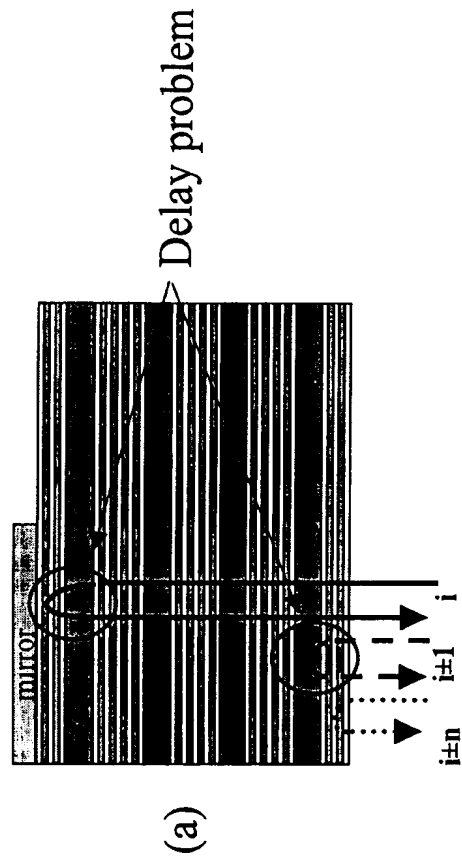


FIG. 6b

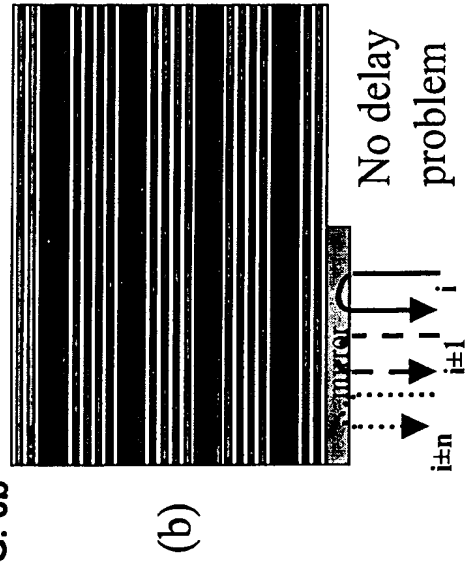
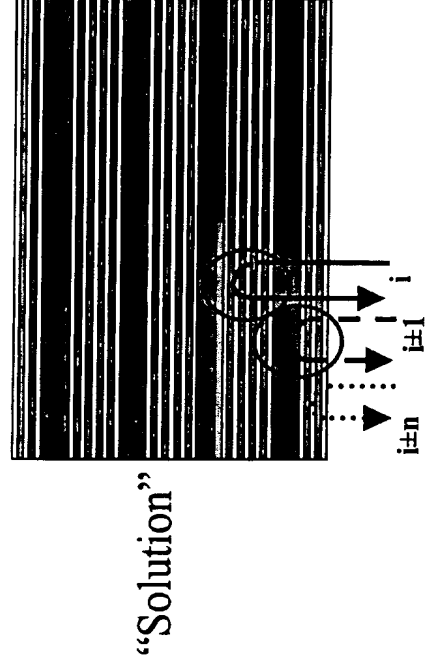


FIG. 6c

Delay problem
minimized



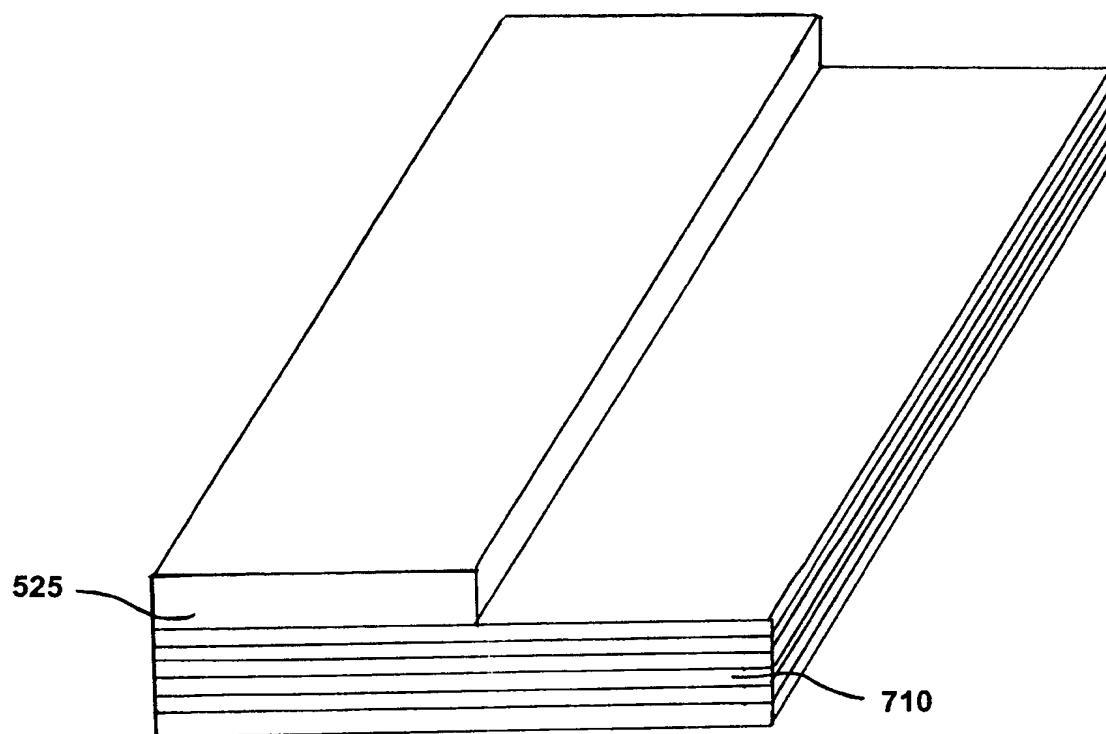


FIG. 7

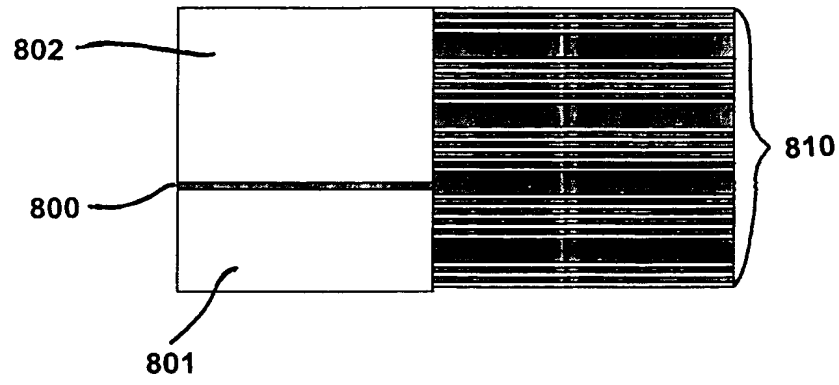


FIG. 8a

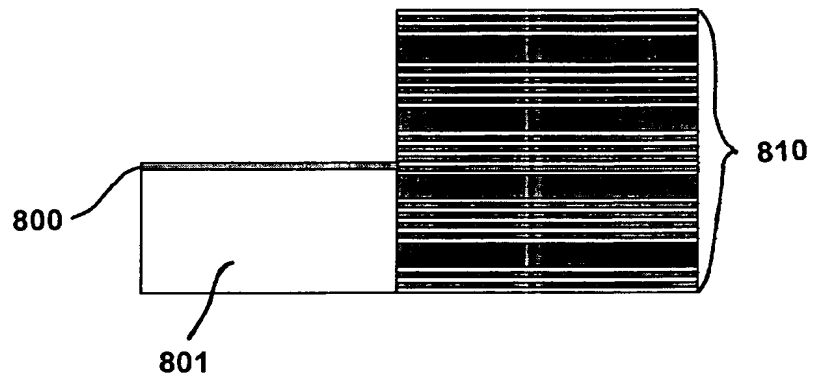


FIG. 8b

Fig. 9a

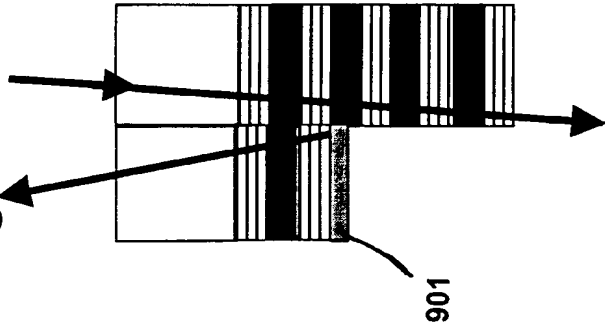


Fig. 9b

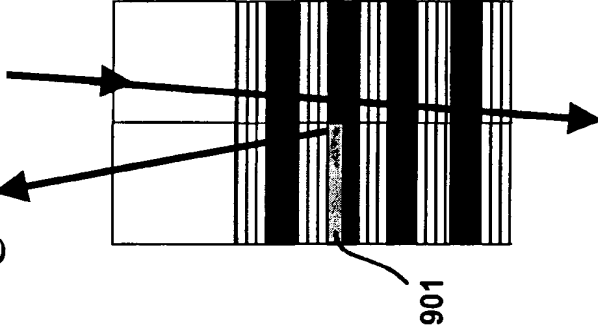


Fig. 9c

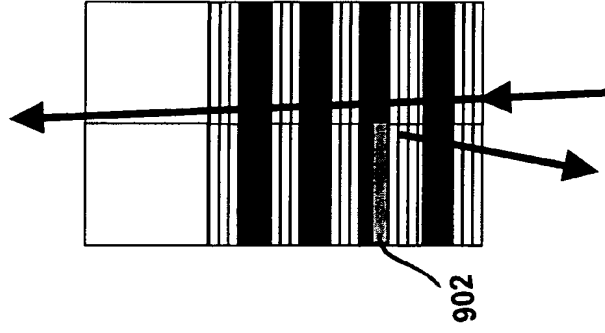


Fig. 10a

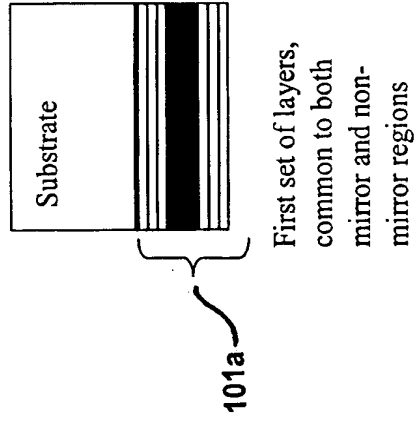


Fig. 10b

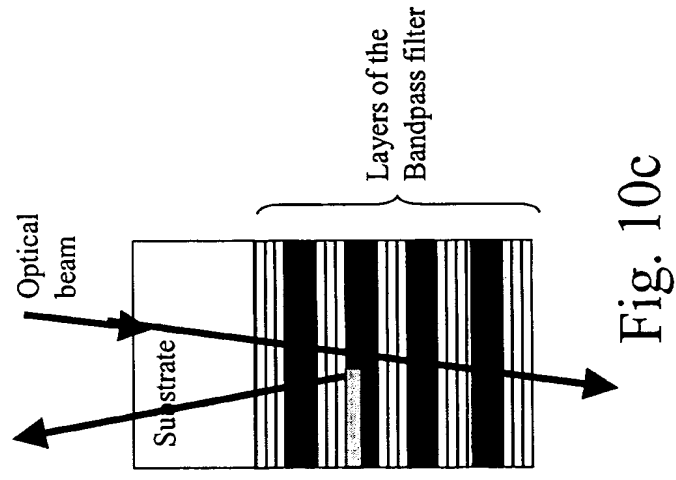
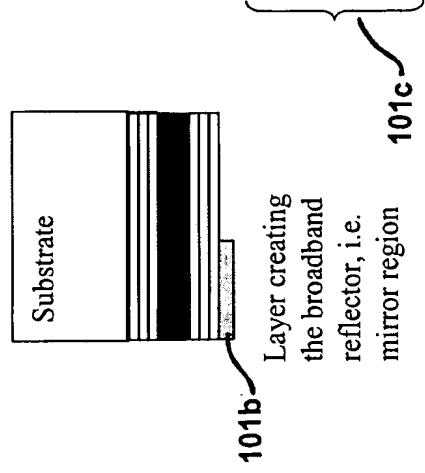


Fig. 10c